

ABSTRACT OF THE DISCLOSURE

5 A current signal corresponding to the amount of
incident light detected by a photoelectric conversion device
13 is inputted to and integrated by an integrator circuit
30, whereby a voltage signal is outputted from the integrator
circuit 30. When a switch 40 is closed, the voltage signal
outputted from the integrator circuit 30 is inputted to a
capacitor 51 of a variable capacity integrator circuit 50,
a change of the voltage signal is inputted to an amplifier
10 52, and an electric charge corresponding to the change of
voltage signal and the capacity value of a variable capacity
part 53 flows into the variable capacity part 53. The
capacity value of the variable capacity part 53 is controlled
by a comparator 60 and a capacity control section 70 such
15 that the value of integrated signal outputted from the
variable capacity integrator circuit 50 coincide with a
reference value. The capacity control section 70 outputs
a first digital signal corresponding to the capacity value
of the variable capacity part 53. As a consequence, a
20 solid-state imaging device which is excellent in S/N ratio,
yields no offset errors even when its amplifier have offset
fluctuations, and has a small circuit scale is obtained.

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